

CONTACT INFORMATION	Email: pchandran@utexas.edu	Linkedin: linkedin.com/in/pranav-chandran
EDUCATION	University of Texas , Austin, TX <ul style="list-style-type: none"> • Ph.D. in Electrical Engineering - Power electronics and machine drives 	2023-2026 (expected)
	University of Washington , Seattle, WA <ul style="list-style-type: none"> • Ph.D. in Electrical Engineering • M.S in Electrical Engineering, GPA: 3.84/4 • Advisor: Prof. Brian B. Johnson. 	2019–2023 2019-2021
	SRM Institute of Science and Technology , India. <ul style="list-style-type: none"> • B.Tech., Electrical and Electronics Engineering GPA: 9.52/10 	2015–2019
COURSEWORK	<ul style="list-style-type: none"> • Linear Integrated Circuits, Circuit theory, Signals and Systems, Power electronics design, Digital control of Power electronics, Control Systems Theory, Linear Multivariable Control, Resonant power converters, Power System Analysis 	
TECHNICAL SKILLS	<ul style="list-style-type: none"> • <i>Design and Simulation tools</i>: MATLAB-Simulink, PLECS, Altium Designer, LTspice • <i>Data acquisition tools</i>: LabVIEW, NI-Virtual bench, NI-DAQ • <i>Microcontrollers</i>: TI - Piccolo (TMS320F280069, TMS320F28388), NI - Compact RIO (cRIO), STM32G4xx series • <i>Programming Languages and Validation tools</i>: C, Python, FMEA, RCA 	
RESEARCH EXPERIENCE	<ul style="list-style-type: none"> • Graduate Research Assistant – UW Jun 2020- May 2022 <ul style="list-style-type: none"> ○ Controls - Design and implementation of digital control for shaft coupled 30kW axial flux Permanent Magnet machines. ○ Hardware and PCB Design - 15 kW T-Type DC-AC converter design for Variable Frequency Drives. ○ Hardware Testing - Functional testing of low power isolated gate driver circuitry, open loop and closed loop power converter tests. • Research intern – ABB Corporate Research Center - Raleigh, NC May - Dec 2022 <ul style="list-style-type: none"> ○ Controls - Design and implementation of digital speed sensor-less control, Field-oriented control (FOC) of low voltage, 2HP Integrated Motor Drives (IMD). ○ PCB Design - Control card design for low voltage IMDs. ○ Hardware Testing - Verification of sensor-less control design on 20HP dynamometer setup. 	
TEACHING EXPERIENCE	<ul style="list-style-type: none"> • Graduate Teaching Assistant – UW 2020 <ul style="list-style-type: none"> ○ Power electronics design - Bill of Materials (BOM) design , kit preparation and class instructor for transistor level analog design of low power boost converter. ○ Power electronics controls - Office hours, lab class instruction for embedded control systems design and Real Time simulation of electric bike drives using TI DSP and PLECS RT box. ○ Power electronics drives capstone - Office hours, lab class instruction for PCB design using Altium Designer, and experimental verification. • Graduate Teaching Assistant – UT Austin Jan-May 2023 <ul style="list-style-type: none"> ○ Resonant power converters - Responsibilities include course material development, holding office hours. 	
SELECTED PUBLICATIONS	<ul style="list-style-type: none"> • P. Chandran, <i>et.al</i>, "Equivalent Circuit Models for Closed-loop Multiphysics Drive Systems," 2020 IEEE 21st Workshop on Control and Modeling for Power Electronics (COMPEL), 2020. 	
HONORS	<ul style="list-style-type: none"> • Graduated as department gold medallist from Electrical Engineering department in Bachelors from a batch of 400 students. 2019 	